RING DET
IMPULSE CARD PRES
PHASE 1 OK
PHASE 2 OK
5MHz IN
600kHz IN
PHONE ANSWER ENABLE
IMPULSE ENABLE
1/2 HOUR TONE
HOUR TONE
FRAME COUNT
SECS
MINS
HOURS
PARALLEL BCD TIME OUTPUT
SERIAL TIME LATCH
SERIAL TIME CLOCK
SERIAL TIME DATA
60Hz
IMPULSE CLKS STOPPED
1Hz
TONE PRESENT
PHASE 1 DRIVE
PHASE 2 DRIVE
AURAL TIME MARKERS
TIMECODE 600Ω
TIMECODE LO-Z
TIME VALID
MODEM TXD
SEIZE LINE
RS232 CTS
RS232 TRANSMIT DATA (RXD PIN)
RS232 DSR,DCD
EXT STB
MODEM CARD PRES
SECONDARY OFFSET
SPARE
RS232 RECEIVE DATA (TXD PIN)
RS232 RTS
EXT REF
MODEM CD
PHONE SYSTEM MODE ENABLE
KYBD BEEP ENABLE
MODEM RXD
GROUND
KYBD STROBE
TO CA
DTMF CODE
DTMF ENABLE
MODEM OUT
MODEM ALB

3 LAST REFERENCE DESIGNATIONS USED: IC46, Q4, D3, D2 NOT USED.
2 ALL CAPACITORS ARE IN MICROFARADS
1 ALL RESISTORS ARE IN OHMS, ±5%, 1/4W
NOTES(UNLESS OTHERWISE SPECIFIED)

LEITCH
MODEL 5300ET
ELECTRONIC TIMER
SCHEMATIC
E
5300ET-C

13
5300ET. 06.88